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Description



Method for setting up a program-controlled circuit arrangement and circuit arrangement for execution of the
5 method.

The present invention relates to a method for setting up a program-controlled circuit arrangement with a processor unit, an assigned non-volatile start procedure memory, a
10 main memory and an interface for connection to a data transmission network as well as a circuit arrangement set up for execution of the method.

The term "program-controlled circuit arrangements" is
15 understood to mean all devices, which need a program or operating program instructions for operation. Usually these are circuit arrangements with a microprocessor, a micro controller or circuit arrangements, which contain a microprocessor core. The prior art is to provide a memory
20 within such circuit arrangements, in which the methods necessary for operation of the circuit arrangement are stored and to set up the processor unit in such a way that on start-up of the circuit arrangement the instructions are loaded and implemented by the processor unit, in order to
25 move the circuit arrangement into a condition ready for use. Numerous devices in the field of entertainment electronics, in the field of household appliances and other small electrical equipment, in which the necessary
30 functions can also be operated cost-effectively with a microprocessor or micro-controller, operate according to the aforementioned principle.

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Even though the operating program instructions necessary for such applications are not particularly extensive, the memory to be provided in the circuit arrangements for storage of these instructions represents an additional
5 complexity and cost factor. In addition an operating program embedded in the circuit arrangement has the disadvantage that updating or changing the operating program instructions requires direct intervention in the circuit arrangement.

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The object of the present invention therefore is to create a method for setting-up a program-controlled circuit arrangement as well as a circuit arrangement set up to carry out method, in which the complexity of the circuit
15 arrangement can be reduced and greater flexibility can be achieved when setting up the circuit arrangement.

This object is achieved according to the invention by a method with the features of claim 1 or a circuit
20 arrangement with the features of claim 8. The sub-claims in each case define preferred and advantageous embodiments of the present invention.

Here the invention exploits the fact that circuit
25 arrangements with an interface for connection to a data transmission network can in any case receive data from the data transmission network. In this way the circuit arrangement can be set up with very little complexity in such a way that it receives the operating program
30 instructions, which are necessary for operation of the circuit arrangement via the data transmission network. Although this requires an operating program server, which is set up accordingly, an overall cost saving results

however when a sufficiently large number of circuit arrangements are supplied with operating program instructions via the data transmission network.

Additionally there is now no longer any requirement to

5 directly intervene in the circuit arrangement following a change in the operating mode of the circuit arrangement as a result of a change in the operating program instructions. In the case of several circuit arrangements of this type there is the additional advantage that, irrespective of the
10 number of circuit arrangements, intervention, that is to say in the operating program server, is necessary only once.

In an advantageous development it can be proposed that the

15 start procedure itself further does not contain the instructions for making connection via the data transmission network, but only those for loading a download procedure from a separate download procedure memory. In this case the processor unit would implement the start
20 procedure from the start procedure memory and at the same time load and execute the download procedure from the download procedure memory and only thereupon make the connection by means of the interface via the data transmission network to the operating program server and
25 load the operating program instructions into the main memory. The advantage here is that the start procedure memory can be smaller, since the instructions for downloading the operating program instructions are held in a separate memory. This in particular is the case where the
30 start procedure memory is integrated together with the processor unit in a semiconductor and the intention is for the start procedure memory to be small. This may for example be for cost reasons, since the start procedure

memory requires space according to its size on the common semiconductor and thus increases its costs.

If a separate download procedure memory is provided, this can be integrated together with the processor unit in a

5 semiconductor, however the download procedure memory is implemented as a separate memory location for the download procedure advantageously as a separate semiconductor module, which in particular is a standard memory module.

This in particular has a serial data output, so that only a

10 few feeder lines are needed between the semiconductor with the processor unit and the download procedure memory. In particular the download procedure memory can be a serial EEPROM.

15 The present invention in principle is suitable for all devices with a program-controlled circuit arrangement, which necessarily due to their function have a connection to a data transmission network. This can for example be a telephone or fax apparatus, which transmits its data via a
20 data transmission network. Such a service in the case of telephony data is also known as voice over IP. In principle however other applications would also be conceivable such as switches or hubs used in data transmission networks for example.

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The function of the operating program server can be implemented in different ways. Usually data transmission networks are equipped with central processing units, which provide central services for subscribers connected to the
30 data transmission network, such as for example file servers etc. A central processing unit of this kind can be set up by implementing suitable software in such a way that the circuit arrangements according to the invention are

supplied with the operating program instructions after corresponding inquiry by the central processing unit.

The operating program server however can also be
5 implemented as a separate unit, which is particularly coordinated with specific program-controlled circuit arrangements according to the present invention and can make contact with these without further adjustment. In the latter case circuit arrangements according to the present
10 invention could be introduced into a computer network, without having to make changes to the computer network and computers or central processing units connected to it.

The invention is described in more detail below on the
15 basis of a preferred embodiment with reference to the attached drawing. The only drawing shows a diagram of the structure of a circuit arrangement to provide a telephone service via a data transmission network.

20 The circuit arrangement 1 illustrated in Fig. 1 is a semiconductor for use in a telephone, which transmits speech signals via a data transmission network 7. For this purpose this circuit arrangement 1 set up for Voice over IP has interfaces for connection of necessary external
25 components, such as headphones 8, a telephone receiver 9, a loudspeaker 10 and a microphone 11 and provides the usual features of performance for a telephone.

A processor unit 2 is integrated in the circuit arrangement
30 1 on a common semiconductor with an assigned start procedure memory 3. Furthermore the circuit arrangement 1 has a switch 6, which on the one hand serves as interface between the circuit arrangement 1 and a data transmission

network 7 and on the other hand for transmitting packets in the data transmission network 7. The data transmission network 7 is an Ethernet, whereby with the two lines illustrated the circuit arrangement 1 is connected on the 5 one hand to a personal computer, not illustrated, and on the other hand with the other subscribers of the data transmission network 7. In addition to the usual data packets, which are destined for the personal computer, telephony data packets, which are sent and received by the 10 circuit arrangement 1, are transmitted via the data transmission network 7.

Besides however it is also conceivable that the circuit arrangement 1 is equipped with only one port or connection 15 for connecting to the data transmission network, in order to receive or send telephony data packets. The personnel computer would be connected in such a case to the data transmission network via a separate line.

20 Further components, not illustrated, are contained within the circuit arrangement, which are necessary to provide the telephone service. The components are controlled in this case by the processor unit 2, which however needs an operating program for satisfactory operation. As is usual 25 in the case of processor units, the processor unit 2 is set up in such a way that, after being switched on or after a RESET, it executes the start procedure stored in the start procedure memory 3. This start procedure is set up in such a way that the processor unit 2 loads a download procedure 30 from an external download procedure memory 4, which is connected via a serial data link to the circuit arrangement 1 or the processor unit 2 in it. For this purpose the processor unit 2 loads the instructions to be executed next

into a main memory 5, which is connected to the circuit arrangement 1. The main memory 5 is a synchronous dynamic RAM, which has the advantage that it permits short access times and provides high memory density at low cost.

- 5 Thus first the start procedure is loaded by the processor unit 2 from the start procedure memory 3 and then the download procedure from the download procedure memory 4 into the main memory and is executed from there.
- 10 The download procedure is set up so that the processor unit 2 connects up via the data transmission network 7 by means of the interface 6 to an operating program server, not illustrated, and from this loads the operating program instructions to the main memory 5. The operating program
- 15 server is set up in such a way that it transmits the required operating program instructions via the data transmission network to the circuit arrangement 1 on a corresponding request of the circuit arrangement 1. The operating program instructions transmitted by the operating
- 20 program server are set up so that once loaded into the main memory 5, they move the circuit arrangement 1 into a condition ready for use. For this purpose the operating program instructions have all the functions which are necessary for operation of the circuit arrangement or the
- 25 telephone.

With the aid of the solution according to the invention, in the case of a telephone, which sends or receives speech signals via a data transmission network 7, the memory

- 30 provided in the telephone for operating program instructions can be made substantially smaller. Additionally in this way updating or changing the operating program instructions for example to implement a new

functional feature is considerably simplified, since such a change does not need to be made on the telephone itself, but can be stored centrally by the operating program server. In this way changes in the operating program 5 instructions, which are stored on the operating program server, affect several and in particular all telephones, which are set up in accordance with the present invention.